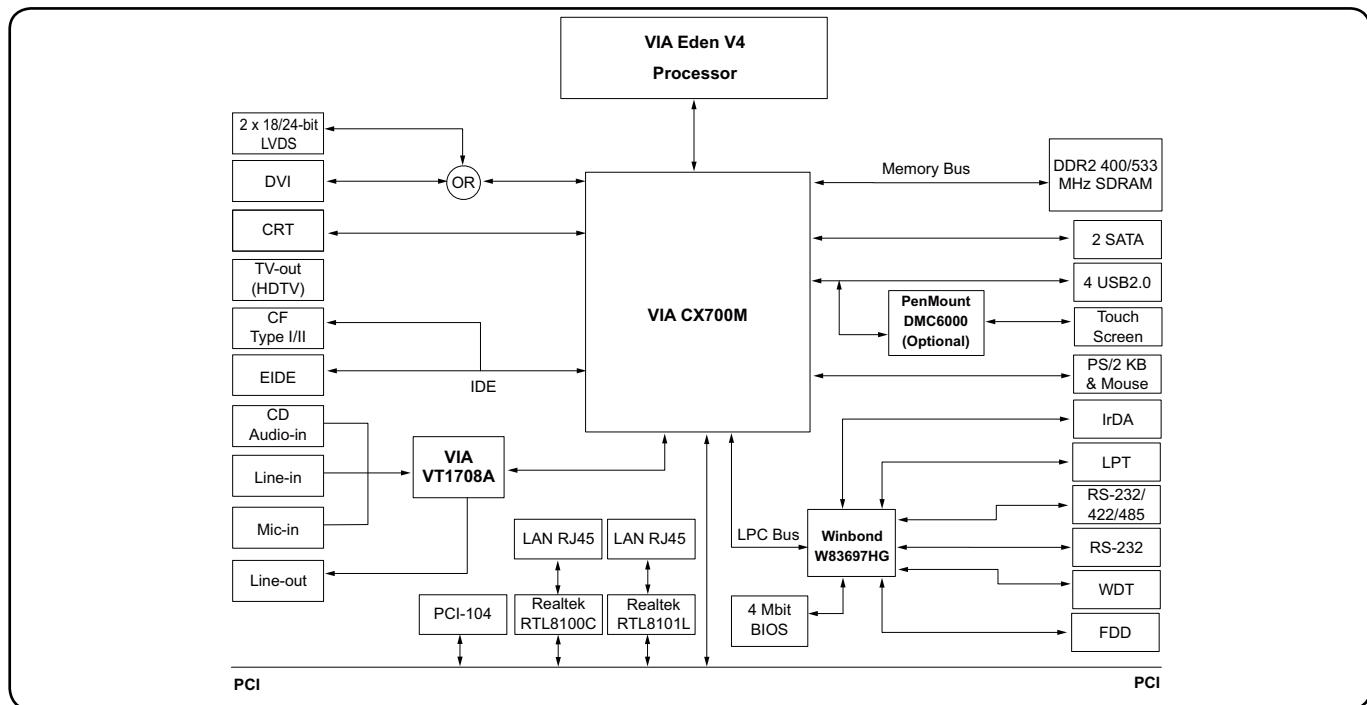


Block Diagram



VIA CX700M Chipset

The VIA CX700M is the most advanced and complete all-in-one x86 system processor for today and next generation computing and media processing platform. From quadruple host data bus, DDR2 memory controller, HDTV interface to Serial ATA and USB ports, the CX700M integrates all the desired, high quality, high performance controller of modern media and computing platforms.

Complementing the power-efficient VIA C7 and fanless VIA Eden processors it supports, the VIA CX700M is based on a highly sophisticated power efficient architecture that enables such rich integration into a compact package with a maximum power envelope of just 3.5 watts. A number of key power management technologies are incorporated that monitor activity and dynamically control power according to system load requirements.

Advanced Memory Controller

The CX700M memory controller supports two DDR DIMMs, up to 1GB system memory. A memory clock buffer is integrated for 1-DIMM system memory architecture. For applications that do not require large system memory or high memory bandwidth, CX700M supports 32-bit memory data width to reduce total system cost while maintaining adequate memory performance. The memory ECC scheme is integrated to improve the system robustness in applications where high reliability is a priority.

Unified Video Decoding Accelerator

The CX700M integrates an industry unique, high performance "Unified Video Decoding Accelerator" for high definition MPEG-4 as well as the

latest WMV9 HD video stream decoding. This feature significantly reduces host processor utilization rate enabling advanced media applications to be implemented without the needs of high frequency CPU, and further reduces the power consumption of the overall platform.

Multiple Display Configurations

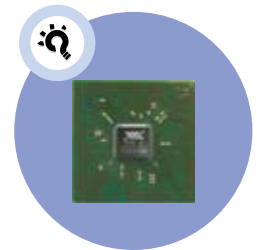
The CX700M integrates a high definition TV Encoder, and supports YpbPr and CompYC TV interface modes through the three RAMDACs. Supported TV resolutions include NTSC - 525i (480i), 525p (480p), PAL - 625i (576i), 625p (576p), and HDTV - 1080i, 720p.

High Definition Audio Interface

A high definition audio controller with up to 32-Bit Sample Size @192KHz Sampling Rate is implemented in CX700M for high-end media applications with up to 7.1 high definition audio channels.

Storage Device Interface

The CX700M integrates the Serial ATA and EIDE Controllers. These two controllers provide maximum flexibility in selecting storage devices (both HD and Optical drives). The Serial ATA controller is Serial ATA II PHY compliant and supports up to two SATA IDE devices with 3Gb/s data transfer rate. The EIDE controller supports up to two EIDE devices in PIO mode 4, multi-word DMA mode 2, and UltraDMA-33/66/100/133 modes.

01
RISC02
ETX03
3.5"04
EPIC05
5.25"06
MB07
PC/10408
Barebones09
Smart
Terminal10
Innoflex
PPC11
ACC